

OFFICE OF THE CONTROLLER OF EXAMINATIONS MAHATMA GANDHI UNIVERSITY NALGONDA - 508 254

Date: 11.02.2022

TIME-TABLE

B.Tech. (CSE/ECE/EEE) III-Semester Regular (AICTE) Examinations February-2022 <u>Time:2.00pm to 5.00pm</u>

Date	Paper	CSE	ECE	EEE
18.02.2022	1	Mathematics – III (Probability and Statistics)	Mathematics – III	Mathematics – III
21.02.2022	11	Digital Logic Design	Managerial Economics and Accountancy	Electrical Circuit – I
23.02.2022	111	Basic Electronics	Electronic Devices and Circuits	Electrical Machines – I
25.02.2022	IV	Data Structures Using C++	Digital System Design	Power Systems – I
28.02.2022	ν	Computer Organization & Architecture	Signal Analysis and Transform Techniques	Electromagnetic Fields
02.03.2022	VI		Network Analysis and Synthesis	Analog Electronics
04.03.2022	VII			Environmental Science

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Controller of Examinations

Copy to:

- 1. The Principal UCET, MGU
- 2. Director, Academic Audit, MGU
- 3. P.S. to Vice-Chancellor, MGU
- 4. P.A. to Registrar



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Date	Paper	AICTE			
		CSE	ECE	EEE	
18.02.2022	l	Principles of Management	Linear Control Systems	Electrical Machines – III	
21.02.2022	11	Fundamentals of Data Sciences	Analog Communication	Measurements and Instrumentation	
23.02.2022	III	Database Management Systems	Digital Communication	Linear Control Systems	
25.02.2022	IV	Software Engineering	Microprocessor & Micro Controller	Microprocessors & Micro Controllers	
28.02.2022	V	Automata Languages & Computation	Professional Elective – I 1. Digital Image Processing 2. Electronic Measurements and Instrumentation 3. Object Oriented Programming Language Systems 4. Digital System Design using Verilog HDL	Power Electronics	
02.03.2022	VI	Professional Elective - I 1.Principles of Programming Languages 2.Advanced Operating Systems 3.Graph Theory	Mandatory Course 1. Technical Communication and Soft skills 2.Indian Constitution 3.Essence of Indian Traditional Knowledge	Open Elective – I 1.Material Handling 2.Disaster Management 3.Geospatial Techniques 4.Operating Systems 5.Reliability Engineering 6.Embedded Systems 7.Digital System Design Using Verilog HDL	

CONTROLLER OF EXAMINATIONS

Thank

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