



OFFICE OF THE CONTROLLER OF EXAMINATIONS
MAHATMA GANDHI UNIVERSITY
NALGONDA - 508 254

TIME-TABLE

Time: 10.00 A.M to 12.00 P.M

B. Tech. (CSE /ECE/EEE) Semester VI-Regular (CBCS)

Date	Paper	CSE	ECE	EEE
15.12.2020	I	Web Technologies	Digital Communication	Electrical Machines-III
16.12.2020	II	Computer Networks	Digital Signal Processing	Microprocessors and Microcontrollers
17.12.2020	III	Object Oriented Analysis Design	Digital System Design Using Verilog HDL	Switchgear and Protection
18.12.2020	IV	Machine Learning	Antennas and Wave Propagation	Digital Signal Processing & Applications
19.12.2020	V	Open Elective-I Digital System Design Using Verilog HDL/Disaster Management/Entrepreneurship Development/Intellectual Property Rights	Managerial Economics and Accountancy	Professional Elective-III Power Quality Engineering/Programmable Logic Controllers/Digital Control Systems
21.12.2020	VI	Professional Elective-II Principles of Programming Languages/Software Architecture & Design Patterns/Microprocessors & Microcontrollers/Simulation & Modeling	Professional Elective-I Digital Image Processing/Computer Organization and Architecture/Optical Fiber Communications/Multi Rate Signal Processing	Open Elective-I Material Handling/Micro – Electro –Mechanical Systems/Disaster Management/Geospatial Techniques/Operating Systems/OOPs Using Java/Embedded Systems/Digital System Design Using Verilog HDL/Reliability Engineering/Industrial Robotics/Intellectual Property Rights/Intellectual Property Rights/Engineering Applications in Medicine

Note:

1. The candidates whose forms are rejected by the Examination Branch should not be examined and if any candidate is found not eligible at the later stage his/her registration will be cancelled for this exam. Any omission or clash may be intimated to the Controller of Examinations, MGU, immediately on the receipt of the Time Table.
2. Cell Phones and programmable calculators are strictly not allowed into the Examination Hall.
3. The responsibility of obtaining correct question paper from the Invigilator at the Examination Hall rests with the candidate. Answering a wrong Question paper may lead to Cancellation of Result.
4. The candidates have to carry the college identity card along with their Hall Ticket during the Examination.

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TIME-TABLE

B. Tech. (CSE /ECE/EEE) Semester V-(CBCS) Backlog Examinations

Time: 2.00 P.M to 4.00 P.M

Date	PP	CSE	ECE	EEE
15.12.2020	I	Database Management Systems	Linear ICs and Applications	Power Systems-II
16.12.2020	II	Design Analysis of Algorithms	Microprocessors and Microcontrollers	Electric Machinery-II
17.12.2020	III	Compiler Design	Analog Communications	Electrical Measurements & Instrumentation
18.12.2020	IV	Software Engineering	Automatic Control Systems	Linear Control Systems
19.12.2020	V	Managerial Economics & Accountancy	Electronic Instrumentation	Elective-I Electrical Distribution System/Renewable Energy Sources/High Voltage Engineering
21.12.2020	VI	Elective Advanced Computer Architecture/Advanced Data Structure/Computer Graphics/Scripting Languages	Elective OOPs using JAVA/Reliability Engineering/Computer Graphics/Disaster Management	Elective-II Power System Reliability/Fuzzy Systems and Genetic Algorithms/Electrical Estimation Costing and Safety

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