



OFFICE OF THE CONTROLLER OF EXAMINATIONS
MAHATMA GANDHI UNIVERSITY
NALGONDA - 508 254

TIME-TABLE

B. Tech. (CSE) VI-Semester CBCS & III-II (NON CBCS) Backlog Examinations

Timings: 10:00 to 12:00 pm

Date	Paper	CBCS	NON CBCS
19.10.2020	I	Web Technologies	Web Programming & Services
21.10.2020	II	Computer Networks	Compiler Constructions
23.10.2020	III	Object Oriented Analysis Design	Principles of Programming Languages
28.10.2020	IV	Machine Learning	Object Oriented System Development
31.10.2020	V	Open Elective-I 1.Digital System Design Using Verilog HDL 2. Disaster Management 3. Entrepreneurship Development 4. Intellectual Property Rights	Computer Networks
02.11.2020	VI	Professional Elective-II 1.Principles of Programming Languages 2.Software Architecture & Design Patterns 3.Microprocessors & Microcontrollers 4.Simulation & Modeling	-----

Note:

1. The candidates whose forms are rejected by the Examination Branch should not be examined and if any candidate is found not eligible at the later stage his/her registration will be cancelled for this exam. Any omission or clash may be intimated to the Controller of Examinations, MGU, immediately on the receipt of the Time Table.
2. Cell Phones and programmable calculators are strictly not allowed into the Examination Hall.
3. The responsibility of obtaining correct question paper from the Invigilator at the Examination Hall rests with the candidate. Answering a wrong Question paper may lead to Cancellation of Result.
4. The candidates have to carry the college identity card along with their Hall Ticket during the Examination.

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TIME-TABLE

B. Tech. (ECE) VI-Semester CBCS & III-II (NON CBCS) Backlog Examinations

Timings: 10:00 to 12 :00 pm

Date	Paper	CBCS	NON CBCS
19.10.2020	I	Digital Communication	Digital Communication
21.10.2020	II	Digital Signal Processing	Digital Signal Processing
23.10.2020	III	Digital System Design using Verilog HDL	Antennas and Wave Propagation
28.10.2020	IV	Antennas and Wave Propagation	Microprocessor and Microcontroller
31.10.2020	V	Managerial Economics and Accountancy	Managerial Economics and Accountancy
02.11.2020	VI	Professional Elective-I 1.Digital Image Processing 2.Computer Organization and Architecture 3.Optical Fiber Communications 4.Multi Rate Signal Processing	-----

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B. Tech. (EEE) VI-Semester CBCS & III-II (NON CBCS) Backlog Examinations

Timings: 10:00 to 12:00 pm

Date	Paper	CBCS	NON CBCS
19.10.2020	I	Electrical Machines-III	Digital Signal Processing
21.10.2020	II	Microprocessors & Microcontrollers	Electrical Machinery-III
23.10.2020	III	Switchgear & Protection	Switchgear and Protection
28.10.2020	IV	Digital Signal Processing and Applications	Microprocessors & Microcontrollers
31.10.2020	V	Professional Elective-III 1.Power Quality Engineering 2.Programmable Logic Controllers 3.Digital Control Systems	Managerial Economics and Accountancy
02.11.2020	VI	Open Elective-I 1. MEMS 2. Disaster Management 3. Geospatial Techniques 4. Operating Systems 5. Embedded Systems 6. OOPS using JAVA 7. Material Handling	-----

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TIME-TABLE

B. Tech. (CSE) IV-Semester CBCS & II-II (NON CBCS) Backlog Examinations

Timings: 2:00 to 4:00 pm

Date	Paper	CBCS	NON CBCS
19.10.2020	I	Computer Oriented Statistics	Mathematics-IV
21.10.2020	II	OOPS Through JAVA	OOPS Through JAVA
23.10.2020	III	Formal Languages & Automata Theory	Microprocessors and Interfacing
28.10.2020	IV	Operating Systems	Data Communications
31.10.2020	V	Data Communication	Environmental Studies
02.11.2020	VI	Basic Electronics	Electrical Circuits and Machines

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B. Tech. (ECE) IV-Semester CBCS & II-II (NON CBCS) Backlog Examinations

Timings: 2:00 to 4:00 pm

Date	Paper	CBCS	NON CBCS
19.10.2020	I	Analog Electronic Circuits	Mathematics-IV
21.10.2020	II	Elements of Mechanical Engineering	Analog Electronic Circuits
23.10.2020	III	Probability Theory & Stochastic Process	Networks and Transmission Lines
28.10.2020	IV	Electromagnetic Theory & Transmission Lines	Signal Analysis and Transform Techniques
31.10.2020	V	Applied Mathematics	Pulse, Digital and Switching Circuits
02.11.2020	VI	Environmental Studies	Environmental Studies

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B. Tech. (EEE) IV-Semester CBCS & II-II (NON CBCS) Backlog Examinations

Timings: 2:00 to 4:00 pm

Date	Paper	CBCS	NON CBCS
19.10.2020	I	Mathematics-IV	Electrical Circuits-II
21.10.2020	II	Electrical Circuits-II	Solid Mechanics
23.10.2020	III	Electricals Machines-I	Power System-I
28.10.2020	IV	Power Systems-I	Electronic Engineering-II
31.10.2020	V	Power Electronics	Electromagnetic Theory
02.11.2020	VI	Linear Integrated Circuits	Electrical Machinery-I
04.11.2020	VII	Prime Movers & Pumps	-----

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