



OFFICE OF THE CONTROLLER OF EXAMINATIONS
MAHATMA GANDHI UNIVERSITY
NALGONDA - 508 254

TIME-TABLE

B. Tech. (CSE /ECE/EEE) V-Semester Backlog (AICTE)Examinations June-July,2022

Timings: 2 to 5 pm

Date	Paper	AICTE		
		CSE	ECE	EEE
20.06.2022	I	Principles of Management	Linear Control Systems	Electrical Machines – III
22.06.2022	II	Fundamentals of Data Sciences	Analog Communication	Measurements and Instrumentation
24.06.2022	III	Database Management Systems	Digital Communication	Linear Control Systems
27.06.2022	IV	Software Engineering	Microprocessor & Micro Controller	Microprocessors & Micro Controllers
29.06.2022	V	Automata Languages & Computation	Professional Elective – I 1. Digital Image Processing 2. Electronic Measurements and Instrumentation 3. Object Oriented Programming Language Systems 4. Digital System Design using Verilog HDL	Power Electronics
01.07.2022	VI	Professional Elective - I 1.Principles of Programming Languages 2.Advanced Operating Systems 3.Graph Theory	Mandatory Course 1. Technical Communication and Soft skills 2.Indian Constitution 3.Essence of Indian Traditional Knowledge	Open Elective – I 1.Material Handling 2.Disaster Management 3.Geospatial Techniques 4.Operating Systems 5.Reliability Engineering 6.Embedded Systems 7.Digital System Design Using Verilog HDL


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Note:

1. The candidates whose forms are rejected by the Examination Branch should not be examined and if any candidate is found not eligible at the later stage his/her registration will be cancelled for this exam. Any omission or clash may be intimated to the Controller of Examinations, MGU, immediately on the receipt of the Time Table.
2. Cell Phones and programmable calculators are strictly not allowed into the Examination Hall.
3. The responsibility of obtaining correct question paper from the Invigilator at the Examination Hall rests with the candidate. Answering a wrong Question paper may lead to Cancellation of Result.
4. The candidates have to carry the college identity card along with their Hall Ticket during the Examination.



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B. Tech. (CSE /ECE/EEE) V-Semester (CBCS) Backlog Examinations, June-July, 2022
Timings: 2 to 5 pm

Date	Paper	CBCS		
		CSE	ECE	EEE
20.06.2022	I	Database Management Systems	Linear IC's and applications	Power Systems-II
22.06.2022	II	Design Analysis of Algorithms	Microprocessors and Microcontrollers	Electrical Machines-II
24.06.2022	III	Compiler Design	Analog Communications	Electrical Measurements & Instrumentation
27.06.2022	IV	Software Engineering	Automatic Control Systems	Linear Control Systems
29.06.2022	V	Managerial Economics & Accountancy	Electronic Instrumentation	Professional Elective-I 1. Electrical Distribution systems 2. Renewable Energy Sources 3. High Voltage Engineering
01.07.2022	VI	Professional Elective – I 1. Advanced Computer Architecture 2. Advance data Structures 3. Computer Graphics 4. Scripting Languages	Open Elective-I 1. OOPs using JAVA 2. Reliability Engineering 3. Computer Graphics 4. Disaster Management 5. Digital System Design using Verilog HDL	Professional Elective-II 1. Power System Reliability 2. Fuzzy Systems & Genetic Algorithms 3. Electrical Estimation Costing & Safety

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B. Tech. (CSE /ECE/EEE) III-I-Semester (Non-CBCS) Backlog Examinations, June-July, 2022
Timings: 2 to 5 pm

Date	Paper	NON CBCS		
		CSE	ECE	EEE
20.06.2022	I	Database management systems	Liner Integrated Circuits and Applications	Power Systems-II
22.06.2022	II	Operating systems	Digital Integrated Circuits and Applications	Electrical Machines-II
24.06.2022	III	Automata Languages and Computation	Analog Communications	Power Electronics
27.06.2022	IV	Software Engineering	Automatic Control Systems	Digital Electronics and Logic Design
29.06.2022	V	Managerial Economics and Accountancy	Computer Organization and Architecture	Liner Integrated Circuits
01.07.2022	VI	Design and Analysis and Algorithms	Electronic Instrumentation	Linear Control Systems

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B.Tech.(CSE/ECE/EEE) III-Semester Backlog (AICTE) Examinations June-July, 2022

Timings: 2 to 5 pm

Date	Paper	CSE	ECE	EEE
21.06.2022	I	Mathematics – III (Probability and Statistics)	Mathematics – III	Mathematics – III
23.06.2022	II	Digital Logic Design	Managerial Economics and Accountancy	Electrical Circuit – I
25.06.2022	III	Basic Electronics	Electronic Devices and Circuits	Electrical Machines – I
28.06.2022	IV	Data Structures Using C++	Digital System Design	Power Systems – I
30.06.2022	V	Computer Organization & Architecture	Signal Analysis and Transform Techniques	Electromagnetic Fields
02.07.2022	VI	--	Network Analysis and Synthesis	Analog Electronics
04.07.2022	VII	--	--	Environmental Science

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B.Tech. (CSE /ECE/EEE) III- Semester Backlog (CBCS) Examinations, June- July, 2022
Timings: 2 to 5 pm

Date	Paper	CSE	ECE	EEE
21.06.2022	I	Mathematics-III	Electrical Devices	Mathematics-III
23.06.2022	II	Mathematical Foundations of Computer Science	Switching Theory and Logic Design	Electrical Circuits-I
25.06.2022	III	Data Structures using C++	Signal Analysis & Transform Techniques	Electromagnetic Fields
28.06.2022	IV	Computer Organization	Networks Analysis and Synthesis	Digital Electronics & Logic Design
30.06.2022	V	Digital Logic Design	Pulse and Digital Circuits	Electronic Engineering-II
02.07.2022	VI	Environmental Studies	Mathematics-III	Environmental Studies
04.07.2022	VII	-----	-----	Managerial Economics & Accountancy

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B. Tech (CSE /ECE/EEE) II-Year I-Semester Backlog (Non-CBCS) Examinations June- July, 2022

Timings: 2 to 5 pm

Date	Paper	CSE	EEE	ECE
21.06.2022	I	Mathematics-III	Mathematics-III	Mathematics-III
23.06.2022	II	Data Structures Using C++	Electrical Circuits-I	Basic Circuit Analysis
25.06.2022	III	Discrete Structures	Environmental Studies	Electromagnetic Theory
28.06.2022	IV	Logic & Switching Theory	Electrical Measurements & Instruments	Electronic Devices
30.06.2022	V	Computer Architecture	Electronic Engineering-I	Elements of Mechanical Engineering
02.07.2022	VI	Basic Electronics	Principles of Mechanical Engineering	Electrical Technology

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