



OFFICE OF THE CONTROLLER OF EXAMINATIONS
MAHATMA GANDHI UNIVERSITY
NALGONDA - 508 254

TIME-TABLE

B. Tech.(CSE /ECE/EEE) III-Semester Backlog (AICTE) Examinations

Timings: 2 to 4 pm

Date	Paper	CSE	ECE	EEE
29.12.2020	I	Mathematics - III (Probability and Statistics)	Mathematics-III	Mathematics-III
30.12.2020	II	Digital Logic Design	Managerial Economics and Accountancy	Electrical Circuits-I
31.12.2020	III	Basic Electronics	Electronic Devices and Circuits	Electrical Machines-I
02.01.2021	IV	Data Structures Using C++	Digital System Design	Power Systems-I
04.01.2021	V	Computer Organization & Architecture	Signal Analysis and Transform Techniques	Electromagnetic Fields
05.01.2021	VI	Network Analysis and Synthesis	Analog Electronics
06.01.2021	VII	Environmental Science

Note:

1. The candidates whose forms are rejected by the Examination Branch should not be examined and if any candidate is found not eligible at the later stage his/her registration will be cancelled for this exam. Any omission or clash may be intimated to the Controller of Examinations, MGU, immediately on the receipt of the Time Table.
2. Cell Phones and programmable calculators are strictly not allowed into the Examination Hall.
3. The responsibility of obtaining correct question paper from the Invigilator at the Examination Hall rests with the candidate. Answering a wrong Question paper may lead to Cancellation of Result.
4. The candidates have to carry the college identity card along with their Hall Ticket during the Examination.

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TIME-TABLET

B.Tech. (CSE /ECE/EEE) III-Semester Backlog (OLD CBCS) Examinations

Timings: 2 to 4 pm

Date	Paper	CSE	ECE	EEE
29.12.2020	I	Mathematics-III	Electronic Devices	Mathematics-III
30.12.2020	II	Mathematical Foundations of Computer Science	Switching Theory and Logic Design	Electrical Circuits-I
31.12.2020	III	Data Structures using C++	Signal Analysis & Transform Techniques	Electromagnetic Fields
02.01.2021	IV	Computer Organization	Network Analysis and Synthesis	Digital Electronics & Logic Design
04.01.2021	V	Digital Logic Design	Pulse and Digital Circuits	Electronic Engineering-II
05.01.2021	VI	Environmental Studies	Mathematics-III	Environmental Studies
06.01.2021	VII	===	===	Managerial Economics & Accountancy

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